

FIG.1 PRIOR ART

DUMMY CELL ARRAY

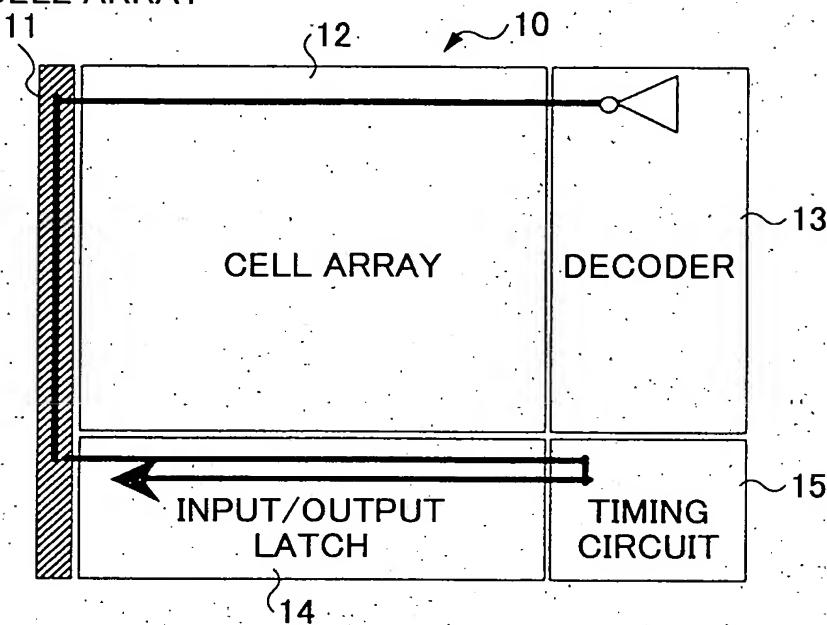
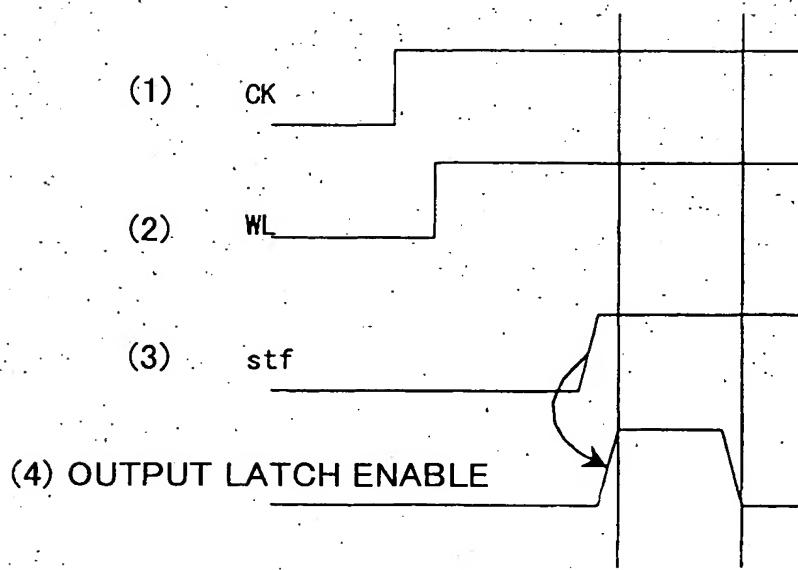


FIG.2 PRIOR ART



Title: SEMICONDUCTOR MEMORY DEVICE
Inventor: ASHIZAWA, et al.
Appln. No.: Unknown
Docket No.: 100353-00178

FIG.3 PRIOR ART

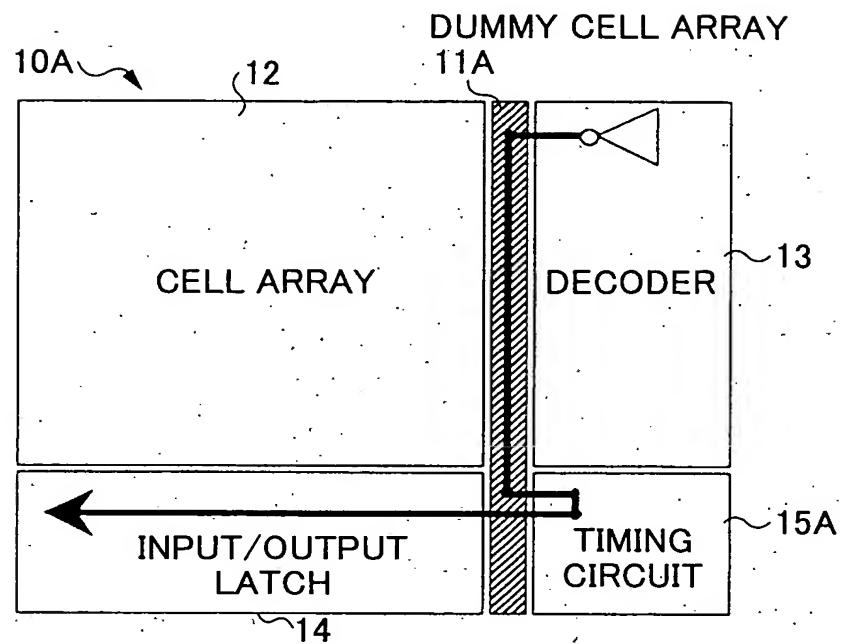
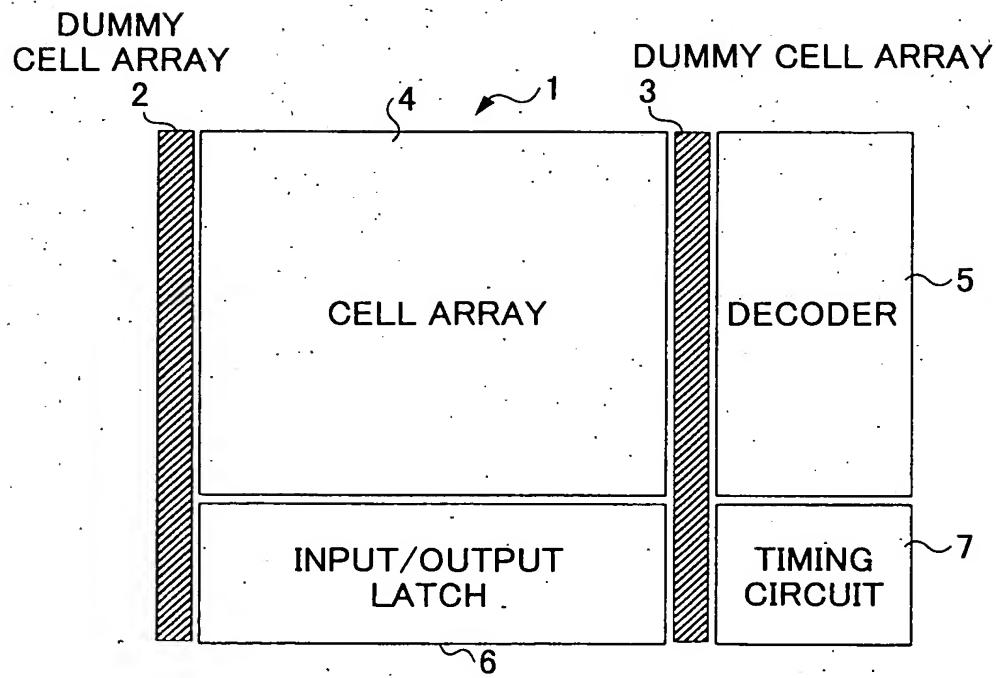


FIG.4



Title: SEMICONDUCTOR MEMORY DEVICE
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FIG.5

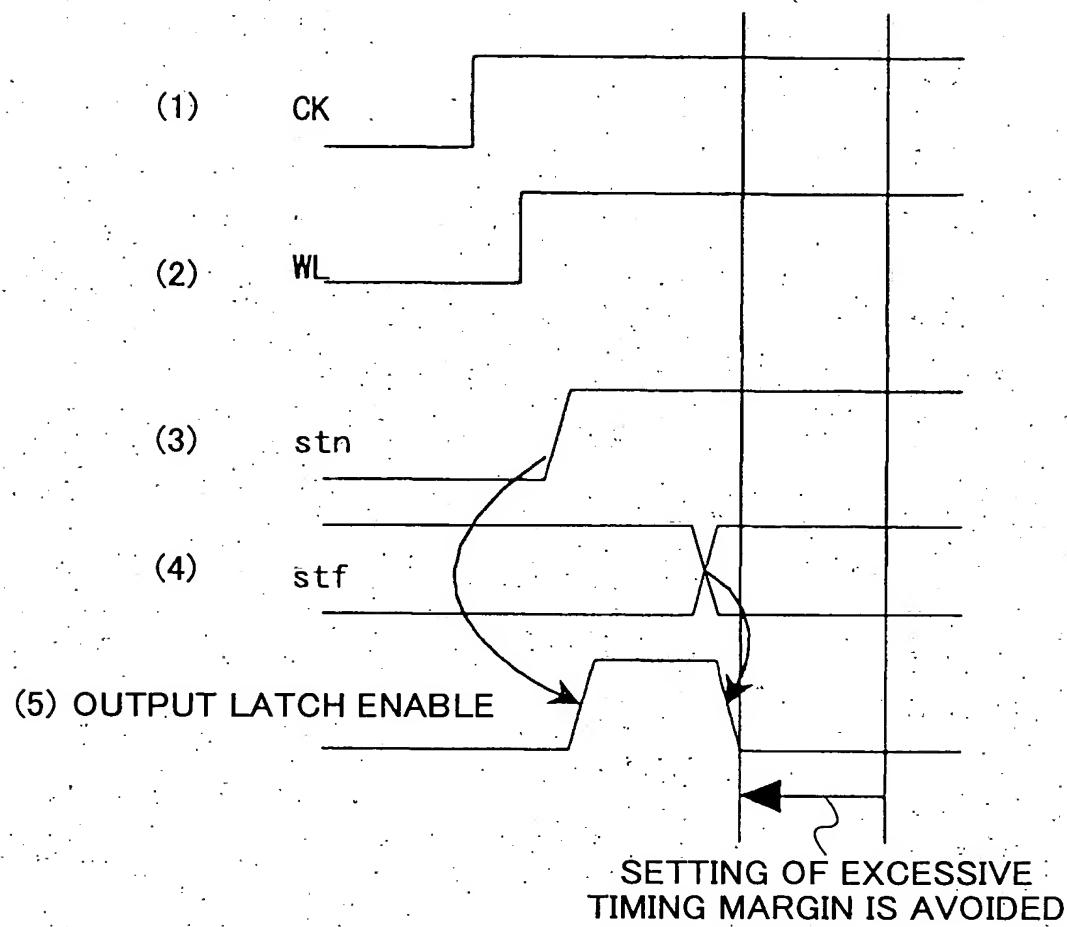
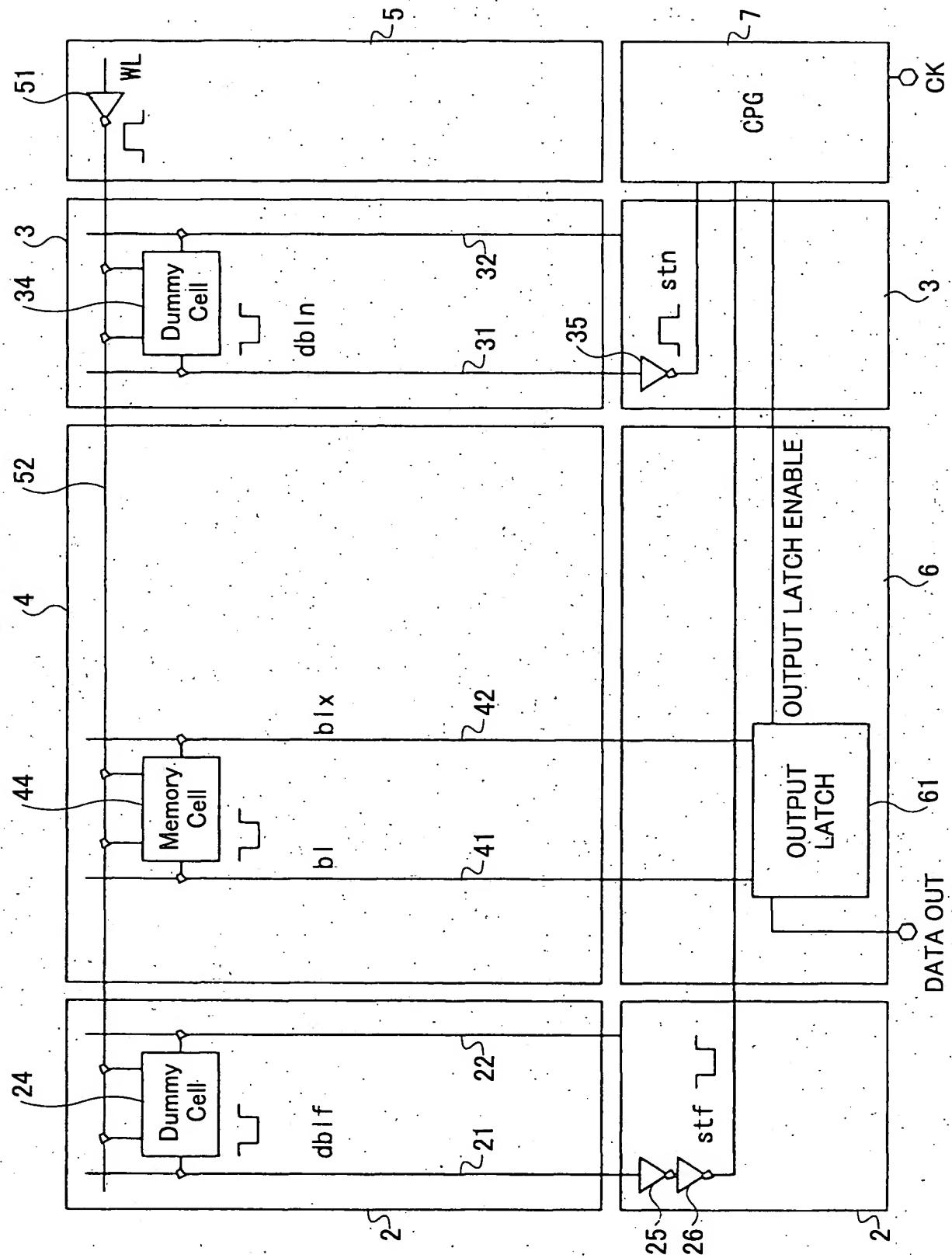


FIG. 6



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FIG.7

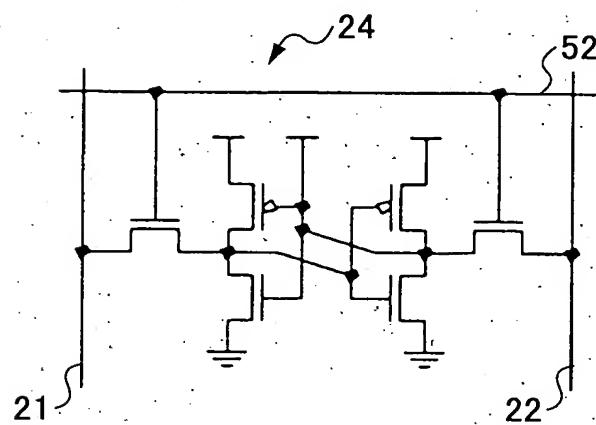
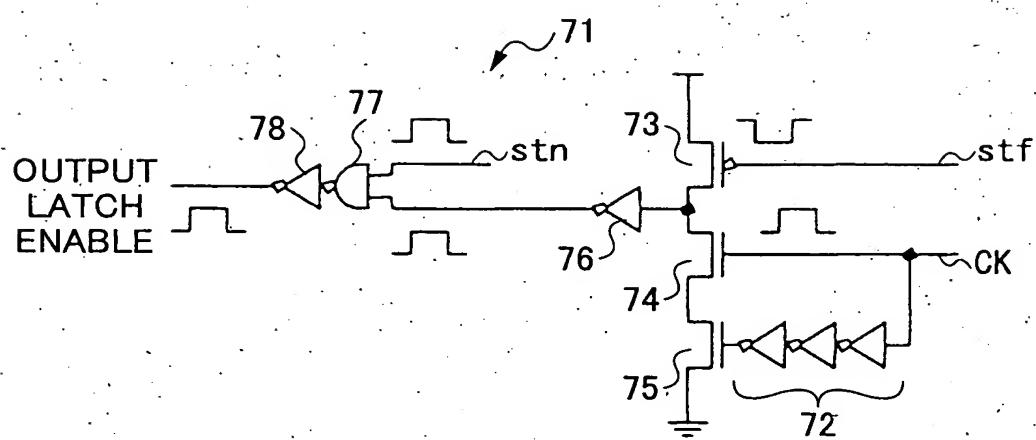
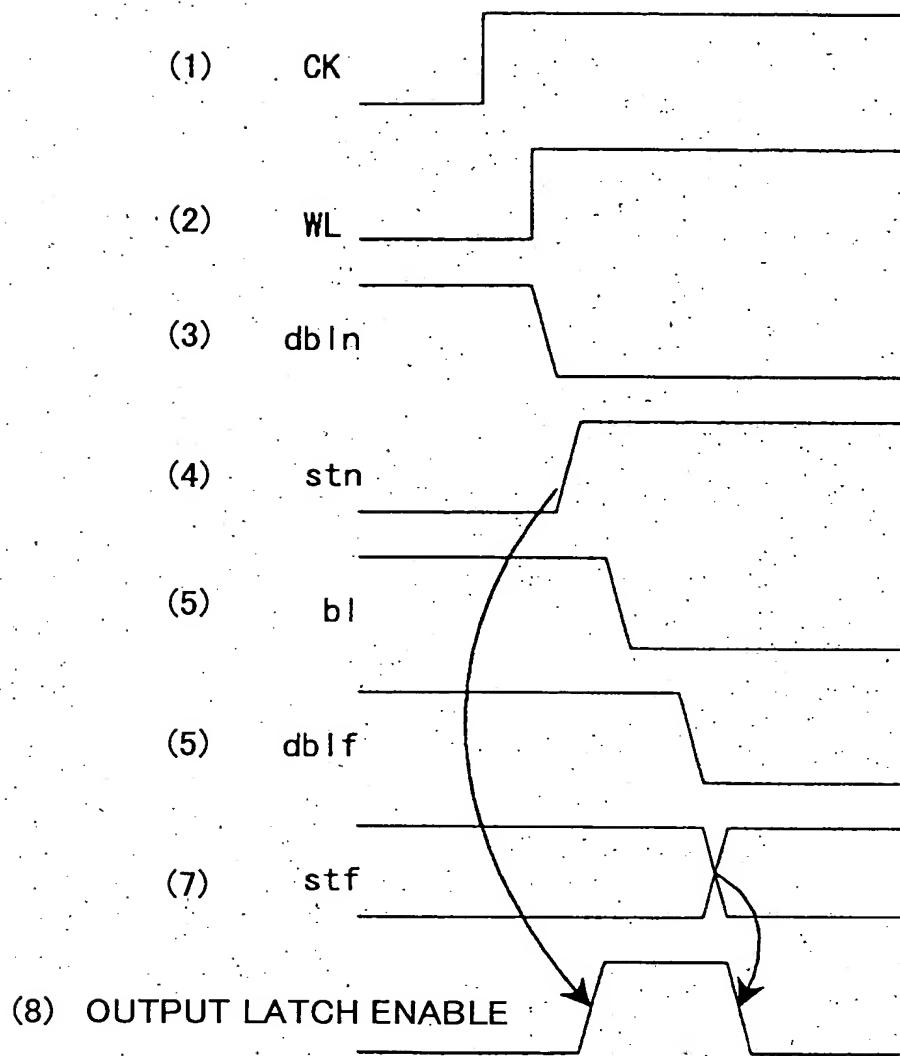


FIG.8



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FIG.9



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FIG.10

